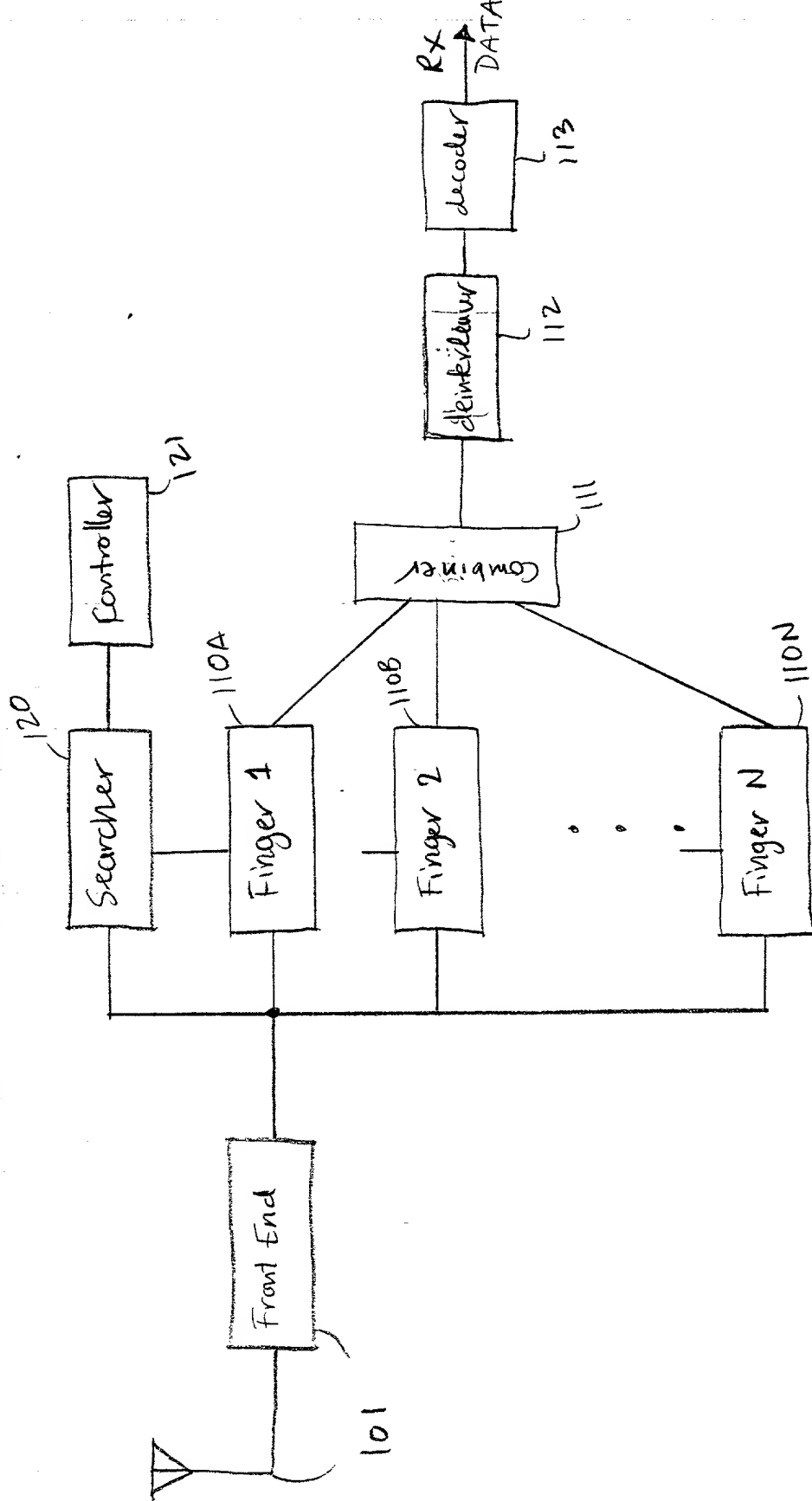


FIG. 1 is a block diagram of a receiver system 100. The system includes a front end 101, a searcher 120, a controller 121, a combiner 111, a demodulator 112, a decoder 113, and a data output 114. The front end 101 is connected to the searcher 120. The searcher 120 is connected to the controller 121. The searcher 120 is also connected to a plurality of fingers 110A, 110B, ..., 110N. The fingers 110A, 110B, ..., 110N are connected to the combiner 111. The combiner 111 is connected to the demodulator 112. The demodulator 112 is connected to the decoder 113. The decoder 113 is connected to the data output 114.



- 100 -

Fig. 1

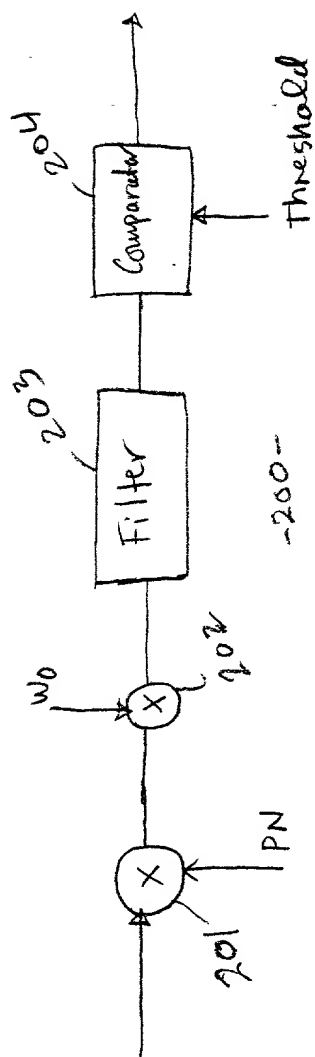


Fig. 2

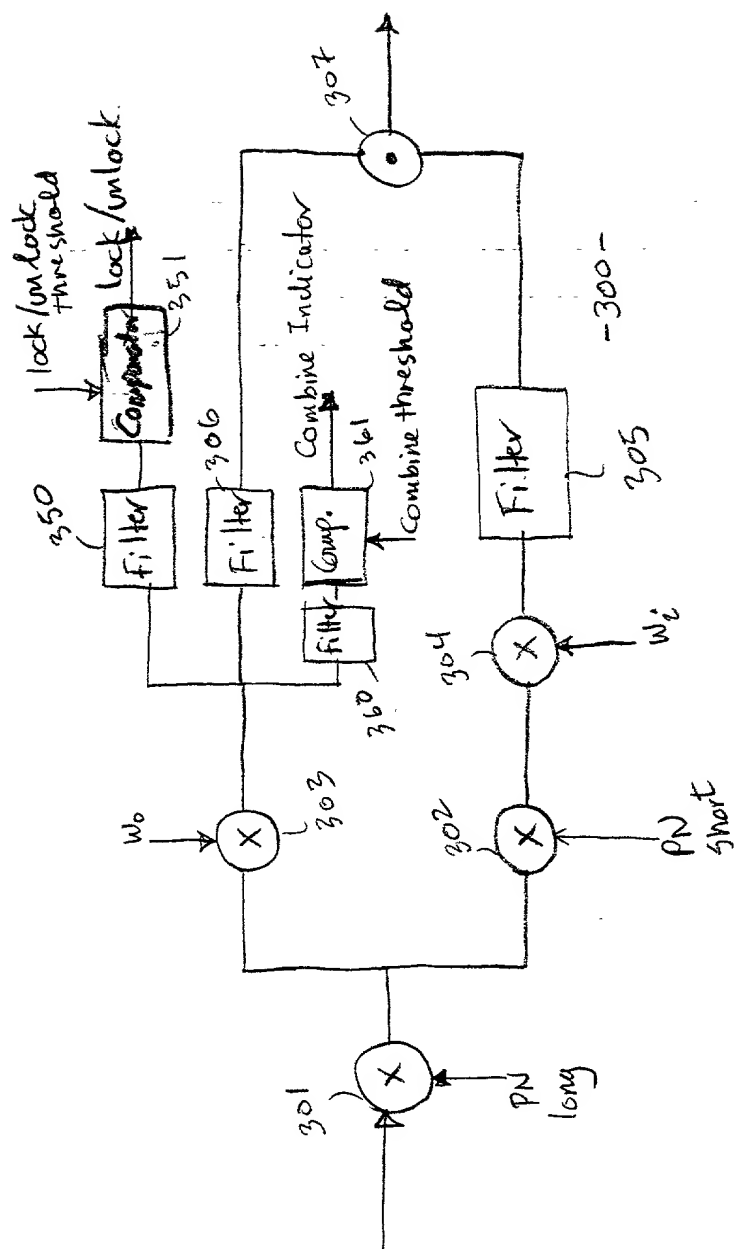
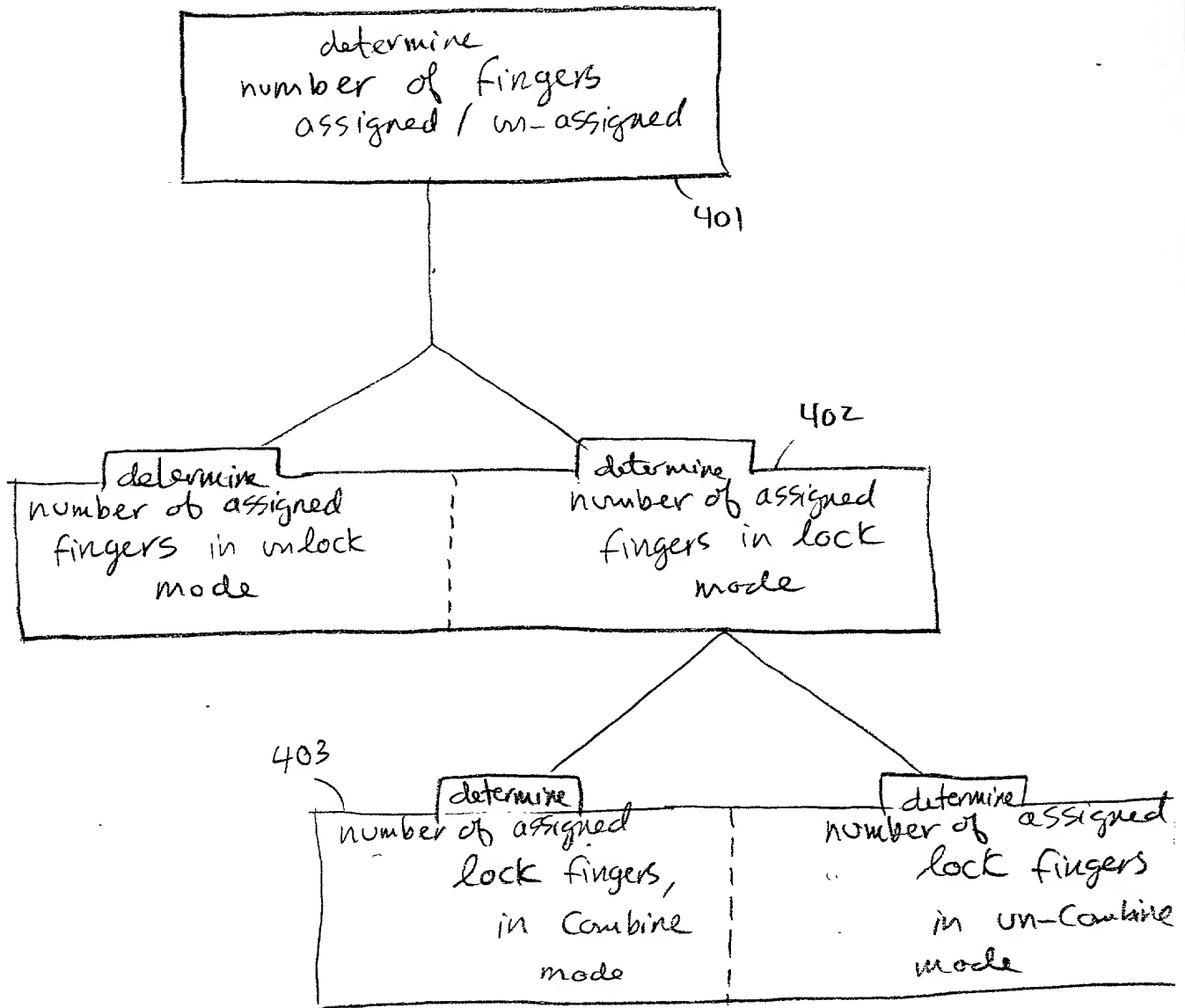


Fig. 3



-400-

Fig. 4

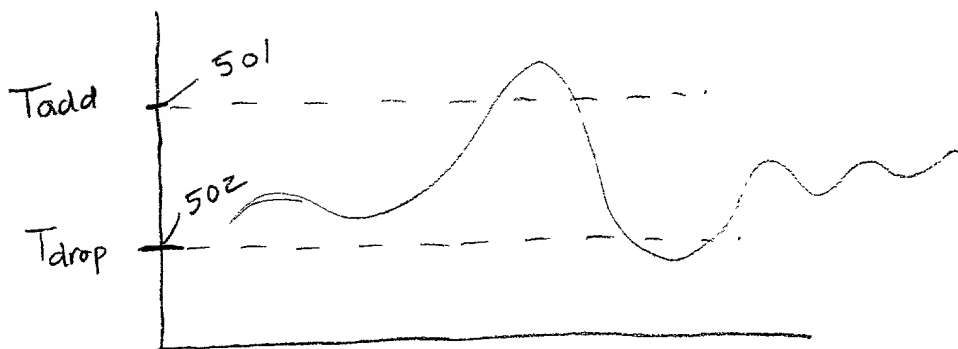


Fig. 5